Notice of Allowability	Application No.	Applicant(s)	
	10/618,425	LEE, BYUNG-JAE	
	Examiner	Art Unit	.)
	Dang T Nguyen	2824	Ar
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in the community or other appropriate communities. This application is sub-	his application. If not include ication will be mailed in due	ed course. THIS
1. This communication is responsive to 11 July 2003.			
2. X The allowed claim(s) is/are <u>1-4</u> .			
3. \boxtimes The drawings filed on <u>11 July 2003</u> are accepted by the Ex	xaminer.		
4.			
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 7/11/03 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Sun Paper No./M 7. ☐ Examiner's A 8. ☑ Examiner's S 9. ☑ Other <u>Search</u>	ail Date mendment/Comment tatement of Reasons for Allo history. Duk Phung	
	P	RIMARY EXAMINER	

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DETAILED ACTION

1. This office action is in response to the following communications: The Application filed and the Information Disclosure Statement filed on July 11, 2003.

2. Claims 1 – 4 are pending on this application. Claims 1 and 4 are independent claims.

Allowable Subject Matter

- 3. Claims 1 4 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

With respect to claim 1, in addition to other elements in the respective claim, the prior art does not teach or suggest "a column address transmitting unit for simultaneously enabling the first and second decoding unit, regardless of a bit select block signal, which selects the first or second unit blocks, of the inputted column address during a test mode; and a first and a second output pads for individually outputting each of outputted signals from the first and second combining circuits".

With respect to claim 4, in addition to other elements in the respective claim, the prior art does not teach or suggest "a test mode controller for simultaneously enabling the first and second decoding unit, regardless of an outputted column address from the counting unit during a test mode; and first and second output pads for individually outputting each of outputted signals from the first and second combining circuits".

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Copper et al. (Patent No.: US 6,144,598) discloses efficiently testing of memory devices.

Nakaoka (Patent No.: 5,864,510) discloses a memory device is tested in a bitcompressed test mode.

Kim (Patent. No.: 5,892,721) discloses an input/output pad for receiving identical data in a test mode and for outputting resultant data after the test.

6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

7. Any inquiry concerning this communication from the examiner should be directed to Dang Nguyen, who can be reached by telephone at (571) 272-1955. Normal contact times are M-F, 8:00 AM - 4:30 PM.

Upon an unsuccessful attempt to contact the examiner, the examiner's supervisor, Richard Elms, may be reached at (571) 272-1869.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist, whose telephone number is (703) 305-3900. The faxed phone number for organization where this application or

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proceeding is assigned is (703) 872-9306.

Information regarding the Status of an application may be obtained from the patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free) or EBC@uspto.gov.

Dang Nguyen 9/22/2004

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